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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re plication of

Filed: May 11, 2001

Serial No.: 09/853,026

For: PROCESS FOR INCREASING THE

EFFICIENCY OF A COMPUTER IN FINITE ELEMENT SIMULATIONS AND

A COMPUTER FOR PERFORMING

THAT PROCESS

PATENT

Group Art Unit: 2123

Examiner:

RECEIVEL OCT 2 4 2001 Technology Center 2100

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, Applicants requests that the publication (copy enclosed), listed on the attached PTO-Form 1449, be made of record in the above-identified application.

Respectfully submitted,

Mark S. Bicks Reg. No. 28,770

Roylance, Abrams, Berdo & Goodman, L.L.P. 1300 19th Street, N.W., Suite 600 Washington, D.C. 20036

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Dated: Oct 22, 2001

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